

## ABSTRACT OF THE DISCLOSURE

A dynamic associative memory device comprising memory cells each of that including: a capacitor connected to one of the bit lines through first transmission gate capable of being switched to ON by an activation of a word line, and having a cell plate supplied with a source voltage; at least one second transmission gate provided in series between the bit lines, capable of being switched to ON by a memory node potential at an opposite side of a source voltage supply-side of the capacitor; and a initializing circuit for controlling the memory node potential upon receiving a reset signal so that at least one of the second transmission gates is switched to OFF.